



Rout_High vs. VDD at multi Iout
DC sweep vs. VDD
Plot $(V(3)-V(2))/I(IO)$ to get output resistance
Repeat load of 10%, 30%, 100% load

Title		
3_Roh_vs_Vdd_Iout		
Size	Document Number	Rev
A	Driver Test Circuit	0
Date:	Friday, October 23, 2009	Sheet 1 of 1